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L1	1723	716/1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:09
L2	1723	(716/1)".ccls" and (current adj conveyor) and ((low adj voltage) adj (current adj mirror))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:14
L3	1723	(716/1)".ccls" and (current adj conveyor) and ((low adj voltage) adj (current adj mirror))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:15
L4	0	(716/1).ccls. and (current adj conveyor) and ((low adj voltage) adj (current adj mirror))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:16
L5	0	("716'/\$).ccls. and (current adj conveyor) and ((low adj voltage) adj (current adj mirror))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:17
L6	2	(current adj conveyor) and ((low adj voltage) adj (current adj mirror))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:17
L7	271	(current adj conveyor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:17
L8	45	((low adj voltage) adj (current adj mirror))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:19
L9	17	(bias adj current) and (((low adj voltage) adj (current adj mirror)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:38

L10	2	(bias adj current) same (((low adj voltage) adj (current adj mirror)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:21
L11	1	(bias adj current) same (((low adj voltage) adj (current adj mirror))) same MOSFET	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:21
L12	2702	323/315	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:31
L13	1825	323/316	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:31
L14	971	711/1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:32
L15	1	(711/1).ccls. and (bias adj current) and (((low adj voltage) adj (current adj mirror)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:33
L16	0	(325/15).ccls. and (bias adj current) and (((low adj voltage) adj (current adj mirror)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:34
L17	0	(325/16).ccls. and (bias adj current) and (((low adj voltage) adj (current adj mirror)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:36
L18	0	("325"/\$).ccls. and (bias adj current) and (((low adj voltage) adj (current adj mirror)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:36
L19	0	(bias adj current) and (((low adj voltage) adj (current adj mirror))) and ((drain adj current) near constant)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 17:38



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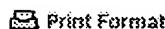
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(low voltage current mirror)

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## 1 Ultra low voltage current mirror op amp and its applications

Rajput, S.S.; Jamuar, S.S.; Circuits and Systems, 2002. APCCAS '02. 2002 Asia-Pacific Conference on , Volume: 1 , 28-31 Oct. 2002 Pages:145 - 148 vol.1

[Abstract] [PDF Full-Text (326 KB)] IEEE CNF

## 2 FGMOS based wide range low voltage current mirror and its applications

Sharma, S.; Rajput, S.S.; Magotra, L.K.; Jamuar, S.S.; Circuits and Systems, 2002. APCCAS '02. 2002 Asia-Pacific Conference on , Volume: 2 , 28-31 Oct. 2002 Pages:331 - 334 vol.2

[Abstract] [PDF Full-Text (354 KB)] IEEE CNF

## 3 Low voltage current mirrors for built-in current sensors

Ramirez-Angulo, J.; Circuits and Systems, 1994. ISCAS '94., 1994 IEEE International Symposium on , Volume: 5 , 30 May-2 June 1994 Pages:529 - 532 vol.5

[Abstract] [PDF Full-Text (280 KB)] IEEE CNF

## 4 Comparison of new and conventional low voltage current mirrors

Ledesma, F.; Garcia, R.; Ramirez-Angulo, J.; Circuits and Systems, 2002. MWSCAS-2002. The 2002 45th Midwest Symposium on , Volume: 2 , 4-7 Aug. 2002 Pages:II - 49-52 vol.2

[Abstract] [PDF Full-Text (328 KB)] IEEE CNF

## 5 Low voltage single-stage amplifier with wide output range

Yang Wang;

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